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Secretary & Correspondent, MITS

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Dr. Sarfraz Hussain, Asst. Prof., Dept. of ECE, MITS
Mr. G. Mahammed Rafi, Asst. Prof., Dept. of ECE, MITS
Ms. S. Ayesha Tanveer, Asst. Prof., Dept. of ECE, MITS

Core Committee members

All the Distinguished Faculty of ECE Department, MITS, Madanapalle

Resource Persons :

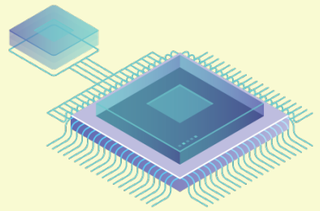
Dr. Nettimi Satya Sai Srinivas,

Director,

ATRIEL India Private Limited,

Karikal.

(Online mode)
A Five Day Workshop
on
Engineering Signal Processing and Communication Systems with FPGA



26th - 30th July 2022

Organized by :
Department of Electronics and Communication Engineering

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC Autonomous)
Angallu, Madanapalle- 517326, Andhra Pradesh, India

About MITS :

Madanapalle Institute of Technology and Science (MITS) is an autonomous institution that was established in 1998 in the picturesque and pleasant environs of Madanapalle and is ideally located on a sprawling 30-acre campus. It has a resort-like atmosphere, which is refreshing and exhilarating at the same time. The UG courses such as ME, CSE, EEE, Civil and ECE as well as PG in MCA and MBA courses are accredited by National Board of Accreditation (NBA). The college is awarded ISO 9001 :2015 certification for the quality policies it is implementing. The campus comprises aesthetically designed buildings that are networked by Wi- Fi technology. This institution is established with modern workshops and labs, computer and internet facilities, seminar halls, a well-equipped library and sports facilities that provide an excellent learning environment for the students. MITS offers UG in Mechanical, Civil, Electrical & Electronics, Electronics & Communications, Computer Science Engineering and PG in MCA and MBA courses.

About the Department :

The Department of Electronics & Communication Engineering started functioning from the academic year 1998 for B. Tech course. The department has distinguished faculty, most of them holding Ph.D. degrees from eminent universities. The Department obtained UGC-Autonomous Status in the year 2014 and is running the program successfully meeting all the requirements. The B.Tech Program under Department of Electronics & Communication Engineering was accredited by the NBA of All India Council for Technical Education (AICTE). The mission of the Department is to enable the students to innovate and excel as eminent academicians, technocrats and entrepreneurs.

Objectives of Workshop

- Introduction to Xilinx Vivado Design Suite (EDA/CAD Tool for Xilinx FPGA-based Digital VLSI System Design)
- Introduction to 7 series Xilinx FPGA family-based beginner level and advanced level development boards (Preferred : Basys3 and ZedBoard).
- From HDL description to FPGA implementation : A complete design flow cycle overview (Preferred HDL : Verilog)
- A hands-on session that includes Verilog coding for simple combinational and sequential circuit design
- Introduction to various types of test benches for digital circuit's functional verification
- Introduction to algorithmic state machine with datapath (ASMD)
- A hands-on session that includes Verilog coding for an example application using the ASMD concept
- A hands-on session to design HEX keypad and 7-segment display controllers.
- Introduction to block-level design
- A hands-on session for developing an example application using Xilinx Intellectual Property (IP) cores, namely virtual input and output (VIO), integrated logic analyzer (ILA), clocking wizard, etc.
- A hands-on session for developing a custom/user-defined IP core-based hardware architecture
- Introduction to hardware/software co-design
- A hands-on session for developing an example application using the concept of hardware/software co-design
- A hands-on session for developing an IEEE 802.3 ethernet packet generator

Who can participate

Professionals from various segments of the Industries, Consultants, University Faculties, Scientists from R&D Institutions, Research Scholars, Students and Corporate Practitioners.

Address for correspondence :

Ms. S. Ayesha Tanveer, : Organizing Committee Member,
Ph. No. : +91 9676269336.
E-mail ID : escfworkshop@gmail.com

Registration Form

Click here for the **Registration Form** or Scan the QR code below

* e-Certificated will be provided to all the registered participants

Important Dates :

Last date of Registration	:	**20th July 2022**
Inauguration	:	**26 th July 2022**
Valedictory	:	**30th July 2022**

Registration fee :

Registration includes access to all the technical and hand-on sessions as well as participation certificate.

UG Students	:	₹ 100
PG Students & Research Scholars	:	₹ 100
Faculty & Industrialists	:	₹ 100

Payment details :

Pay Online- NEFT/RTGS or IMPS in our below given Account. While making online payment, Participants are further requested to mention their transaction ID in the registration form to enable us to trace your payment in the Bank Statement. Also, kindly note to add the applicable bank charges, if any, for online payment.

Bank account details :

Account Name	:	Madanapalle Institute of Technology and Science
Account Number	:	50200004213888
Name of the Bank	:	HDFC Bank Ltd.
IFS Code	:	HDFC0002435
Branch Name	:	CTM Road, Madanapalle-517325

No Cheque/ DD/ Cash payments are accepted.